

MPEG-2 Main Profile Decoder

Version 01.00.17

Release Notes

November 2016

Build ID: 01.00.17.01

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Refer to Fixed In This Release section

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MPEG-2 Main Profile Decoder, which runs on HDVICP2 and Media Controller based SOCs.	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1 for HDVICP2 processor
- Code Generation Tool version 5.0.3 for Media Controller
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.23)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42



- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68

Fixed In This Release

Defect ID	Description	Applicable Release Configuration
CODECS-2427	MPEG2Decoder expects output buffer to be sent for parsing the header.	IVAHD_001

Known Issues

None

MPEG-2 Main Profile Decoder

Version 01.00.16

Release Notes

February 2016

Build ID: 01.00.16.01

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Refer to Fixed In This Release section

Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG-2 Main Profile Decoder, which runs on HDVICP2 and Media Controller based SOCs.	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

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- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.23)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16



– XDC tools version 3.20.04.68

Fixed In This Release

Defect ID	Description	Applicable Release Configuration
OMAPS00331190	Codec sets error in case of 1-referece B-frame in closedGop.	IVAHD_001

Known Issues

None

MPEG-2 Main Profile Decoder



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Version 01.00.15

Release Notes

January 2016

Build ID: 01.00.15.01

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Refer to Fixed In This Release section

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MPEG-2 Main Profile Decoder, which runs on HDVICP2 and Media Controller based SOCs.	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

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- Code Generation Tool version 4.5.1 for HDVICP2 processor
- Code Generation Tool version 5.0.3 for Media Controller
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.23)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68



Fixed In This Release

Defect ID	Description	Applicable Release Configuration
OMAPS00330418	Decoder releases the output buffer twice when Flush is applied before a B frame.	IVAHD_001

Known Issues

None

MPEG-2 Main Profile Decoder

Version 01.00.14



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Release Notes

July 2014

Build ID: 01.00.14.01

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Refer to Fixed In This Release section

Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG-2 Main Profile Decoder, which runs on HDVICP2 and Media Controller based SOCs.	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

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- Code Generation Tool version 4.5.1 for HDVICP2 processor
- Code Generation Tool version 5.0.3 for Media Controller
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.23)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68

Fixed In This Release



Defect ID	Description	Applicable Release Configuration
SDOCM00106454	MPEG2 decoder hangs, when flush is invoked without submitting frames	IVAHD_001

Known Issues

None

MPEG-2 Main Profile Decoder

Version 01.00.13

Release Notes



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September 2013

Build ID: 01.00.13.01

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- MPEG2 Decoder Media Controller library is compiled with Code Generation tools v5.0.3 to produce ELF compliant binaries
- Migration of HDVICP2 API to 01.00.00.21

Validation Information

Release Configuration	Description	Validation Platform
IVAHD_001	MPEG-2 Main Profile Decoder, which runs on HDVICP2 and Media Controller based SOCs.	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

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- Code Generation Tool version 4.5.1 for HDVICP2 processor
- Code Generation Tool version 5.0.3 for Media Controller
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.21)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42
- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68

Fixed In This Release



None

Known Issues

None

MPEG-2 Main Profile Decoder

Version 01.00.12

Release Notes

May 2013

Build ID: 01.00.12.00

The release note is divided into the following sections:

- [New In This Release](#)
- [Validation Information](#)
- [Fixed In This Release](#)
- [Known Issues](#)

New In This Release

- Refer to Fixed In This Release section

Validation Information

Release Configuration	Description	Validation Platform
IWAHD_001	MPEG-2 Main Profile Decoder, which runs on HDVICP2 and Media Controller based SOCs.	DM816x REV-A2 DDR2 EVM [User Guide] [Data Sheet]

This release is validated on DM816x REV-A2 DDR2 EVM using following components

- Code Composer Studio (CCSv4) version 4.2.0.09000
- Code Generation Tool version 4.5.1
- Framework Component version 3.20.00.22
- HDVICP2 API (01.00.00.19)
- HDVICP2 CSP (Containing CSL) version 00.05.02.00
- BIOS version 6.30.02.42



- Codec Engine (CE) version 3.20.00.16
- XDC tools version 3.20.04.68

Fixed In This Release

Defect ID	Description	Applicable Release Configuration
SDOCM00095768	MPEG2 decoder produces a lot of artifacts	IVAHD_001
SDOCM00101437	Same enum names in MJPEG and MPEG2	IVAHD_001
SDOCM00101516	VDMA Tiler Base Address calculation issue	IVAHD_001

Known Issues

None